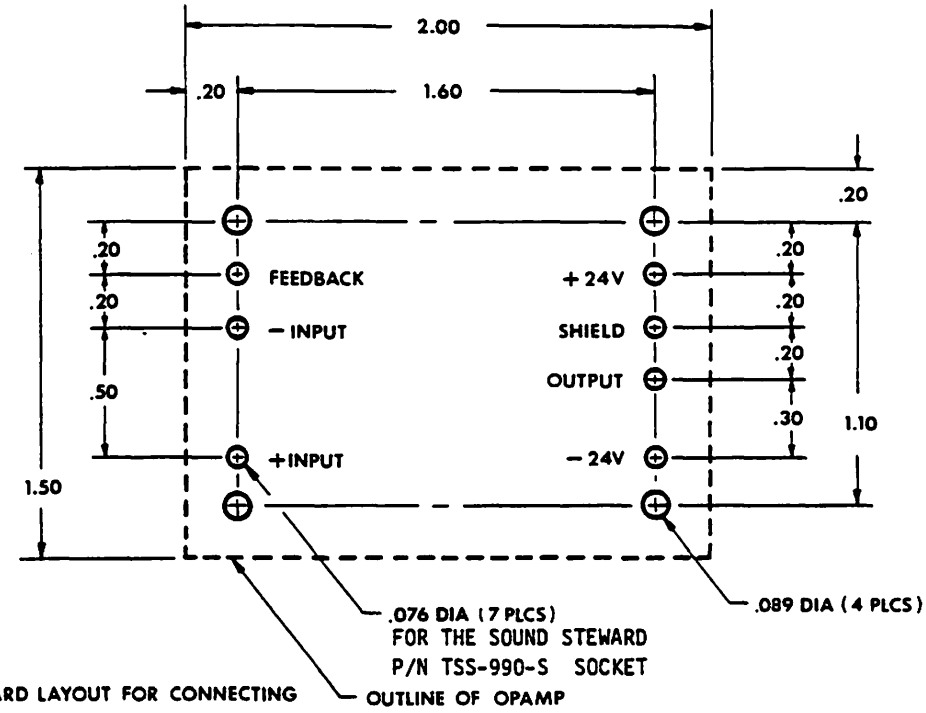

PRINTED CIRCUIT BOARD LAYOUT FOR CONNECTING TO A TSS-990 OPAMP VIEWED FROM TOP SIDE

SPECIFICATIONS:

PARAMETERS	CONDITIONS	VALUE	UNITS
Open Loop Gain	DC to 30 Hz	125	dB
Gain Error	100 dB gain	0.4	dB
Noise Voltage	Per transistor	0.8	nv/root Hz
	Combined	1.13	nv/root Hz
Noise Current		1.0	nA/root Hz
Noise Figure	Rs = 1000 ohms	0.6	dB
Equivalent	Shorted Input	−133.7	dBv Re:0.775 v
Input Noise	BW = 20 kHz		
Max Input Level	Unity gain	+25	dBv Re:0.775v
Input Impedance	Non-Inverting	>10	megohms
Input Bias Current		+2.2	uA
Max Output Level (bipolar 24 volt supply)	RL = 75 ohms	+24.5	dBv Re:0.775v
Max Output Current		260	mA peak
Distortion 20 kHz	RL = 75 ohms		
+24dBv	Gain = 40 dB	0.03	% THD
	Gain = 20 dB	0.004	% THD
	RL = 600 ohms		
	Gain = 40 dB	0.0026	% THD
Slew Rate	RL = 150 ohms	18	V/us
	RL = 75 ohms	16	V/us
Large Signal BW	RL = 150 ohms	145	kHz
Small Signal BW	Unity gain (ft)	10	MHz
Gain BW Product	10 kHz−100 kHz	>50	MHz
Phase Margin	10 MHz	>38	degrees
	<2 MHz	>60	degrees
Response Time	Unity gain	<20	ns
Supply Current	No Load	25	mA
Supply Voltage	Bi-polar	12,15,18 or 24	V

Specifications subject to change without notice or obligation